

## SIMULATED ERROR INJECTION SYSTEM IN TARGET DEVICE FOR TESTING HOST SYSTEM

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### ABSTRACT OF THE DISCLOSURE

A method and device are provided that use a sequencer in the device to control interactions on an interface bus. The sequencer is programmed to interrupt a co-processor before execution of a command. Based on the interrupt signal and a stored error mode page, a false error condition is initiated by further  
10 programming the sequencer to operate abnormally. After recovery from the error condition, the sequencer is reprogrammed to operate normally.